

67,010-095
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There is no motivation for adding the rectifier circuit 4 of the *Yoneta, et al.* reference to the *Latos* arrangement as proposed by the Examiner. If the rectifying circuit could be used as proposed by the Examiner, that would be redundant to *Latos'* arrangement because it already provides an ability to perform the two functions mentioned above. Adding a redundant feature does not provide any benefit. Without any benefit there is no motivation for making the combination. Without any motivation there is no *prima facie* case of obviousness.

This case is in condition for allowance as there is no *prima facie* case of obviousness against any of Applicant's claims. The additional references proposed to be added when rejecting some of Applicant's claims do not remedy the defect in the proposed combination of the *Latos* and *Yoneta, et al.* references. Applicant respectfully requests a Notice of Allowance as soon as possible.

Respectfully submitted,

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CERTIFICATE OF FACSIMILE

I hereby certify that this Response, relative to Application Serial No. 10/806,635, is being facsimile transmitted to the Patent and Trademark Office (Fax No. (571) 273-8300) on August 2, 2006.


Theresa M. Palmateer

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